HOMEWORK #7: More Huffman Circuits

This homework is due by Midnight on Thursday, October 21, 2004.
NO LATE HOMEWORK WILL BE ACCEPTED.

1. Read Chapter 5.
2. Complete Problem 5.9.
   - Begin prime generation with $ac + bd + da$.
   - Note that the library is shown in Figure 5.42, not 5.41.
   - Also, assume that you have the following gates in your library:
     - And-Or of form $f=(a+bc)$ with cost 3 (i.e., $f=\text{nand}(\text{inv}(a),\text{nand}(b,c))$).
     - And-Or of form $f=(ab+cd)$ with cost 4 (i.e., $f=\text{nand}(\text{nand}(a,b),\text{nand}(b,c))$).
     - Buffer with cost 0 (i.e., $f=\text{inv}(\text{inv}(a))$).
5. Write a project proposal. It should include a clear plan of what you intend to do for your project and exactly what you expect to accomplish. It should reference any papers that are related to your project. This should be no more than 1 page long. Email it to myers@vlsigroup.ece.utah.edu.