LAB #3: FSM for a Security Code Access System

Lab writeup is due to your TA at the beginning of your next scheduled lab. Don’t put this off to the last minute! There is pre-lab work to complete before the start of the next lab. NO LATE LAB REPORTS WILL BE ACCEPTED.

1 Objectives

- Use the parallel I/O functions of the 68HC11.
- Learn about usage of FSM abstraction in assembly.
- Design a FSM for a security code access system.

2 Reading

- Read section 2.4 about FSM abstraction
- Read section 3.4.2 about accurate time delays.

3 Background

In this lab, you will use the parallel I/O ports on the 68HC11 to design a security code access system. This device will accept four digits that it will compare with a stored access code. If the digits entered match the code stored, then an LED (LED1) will be turned on to indicate that the lock has been opened. A second LED (LED2) will be used to indicate that the system is ready to accept the first digit. In other words, LED2 will be illuminated initially. After the first digit is entered, it will go off. After the fourth digit has been entered, LED2 will turn on again and at the same time if the proper code has been entered LED1 will also turn on.

In lab 4, you will be using a keypad to enter the digits, but for this lab you can use either switches or even just a wire. To keep it simple, you can assume there are only four possible digits which are one-hot encoded (i.e., one switch or wire will be connected to high at a time). The FSM should determine that a digit is being pressed by seeing one of the four inputs going high. It should then record which digit is pressed and wait for 10ms to debounce the switch. Finally, it should wait for the digit to no longer be pressed (i.e., its input pin goes low). It should repeat this process four times checking along the way whether or not the digits being entered match the hard coded security access code. If all four digits match, then both LEDs are turned on. If any digit does not match, then only LED2 will turn on after all four digits are entered.

4 Tasks

1. Design the FSM for your security code access system.

2. Prepare a schematic for your design including all chips, switches, LEDs, resistors, capacitors, etc.

3. Design the assembly language program for your security code access system. Do not use loop delays, but instead use the TCNT timer to implement the time delays in your FSM. Be sure that it is designed using the software issues discussed in class. For example, it should consist
of separate modules (ex., subroutine for waiting, another for reading which key is pressed, another for waiting for the pressed key to be released, and another for setting the LEDs, etc.). It should also be well documented.

4. Connect your circuit and debug your software.

5  Prelab

Complete tasks 1, 2, and 3 before coming to lab.

6  Writeup

Include the following items. In this lab, only one writeup per team is required.

1. Your FSM diagram.
2. Your hardware schematic.
3. A printout of your assembly code.